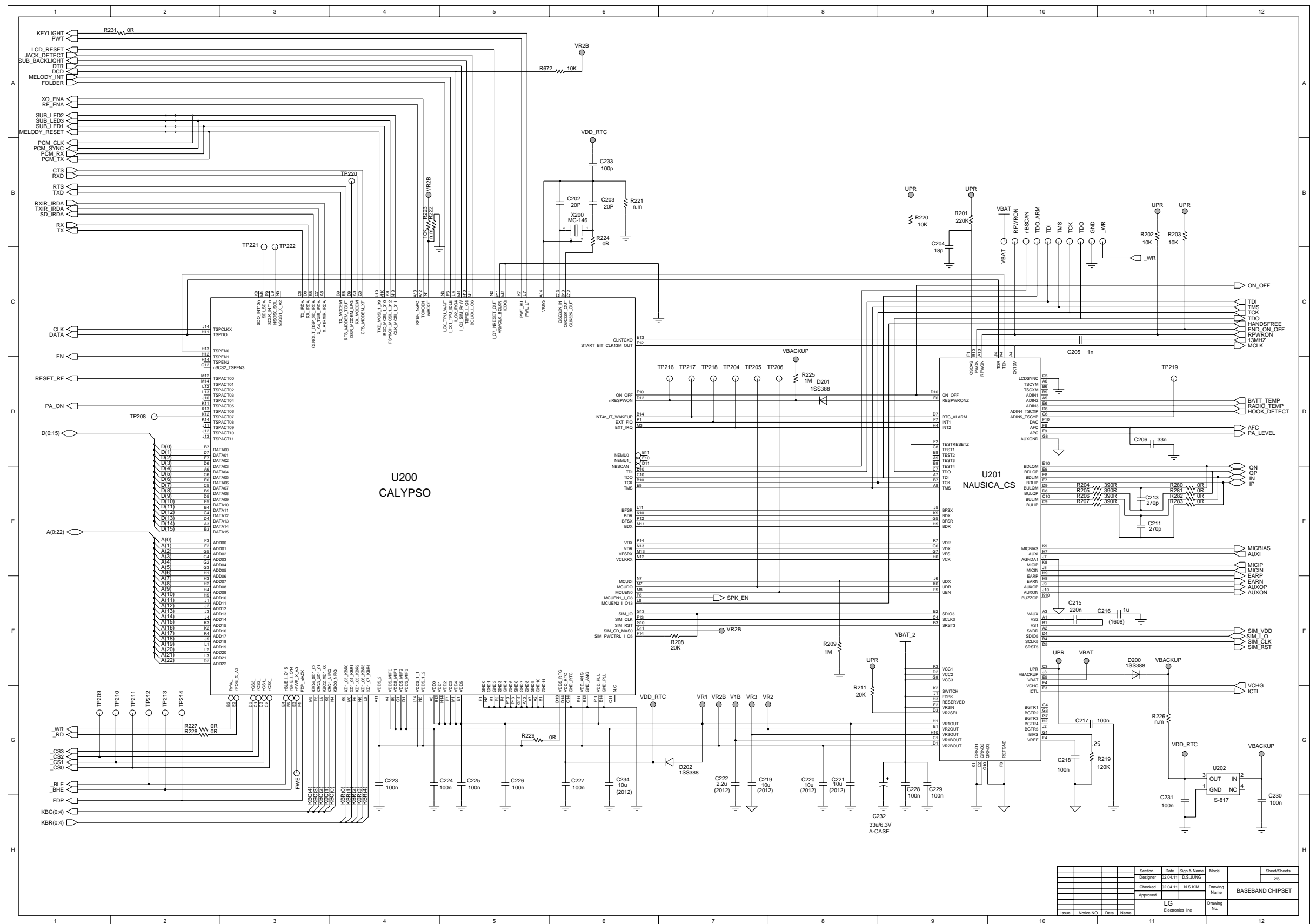
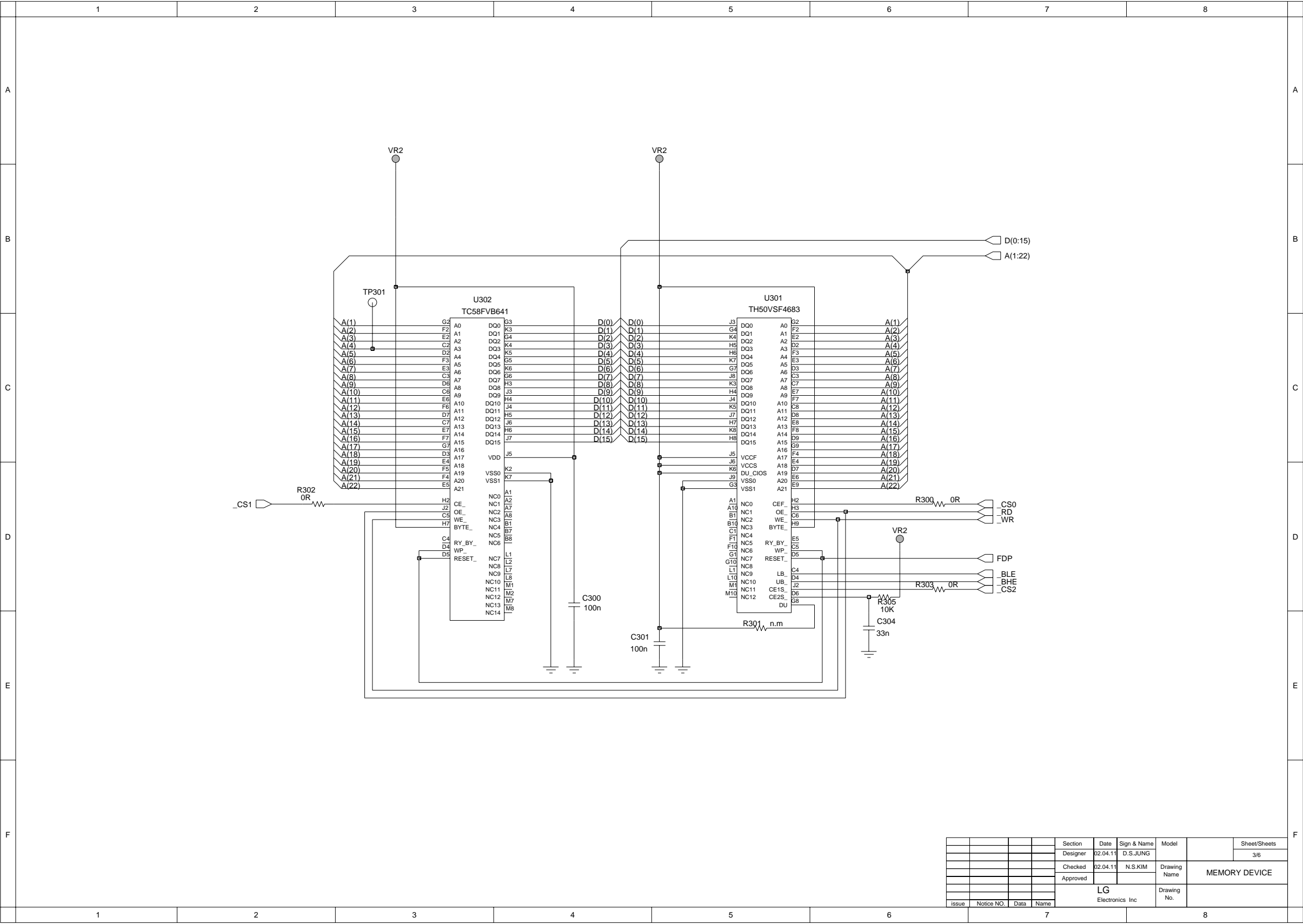


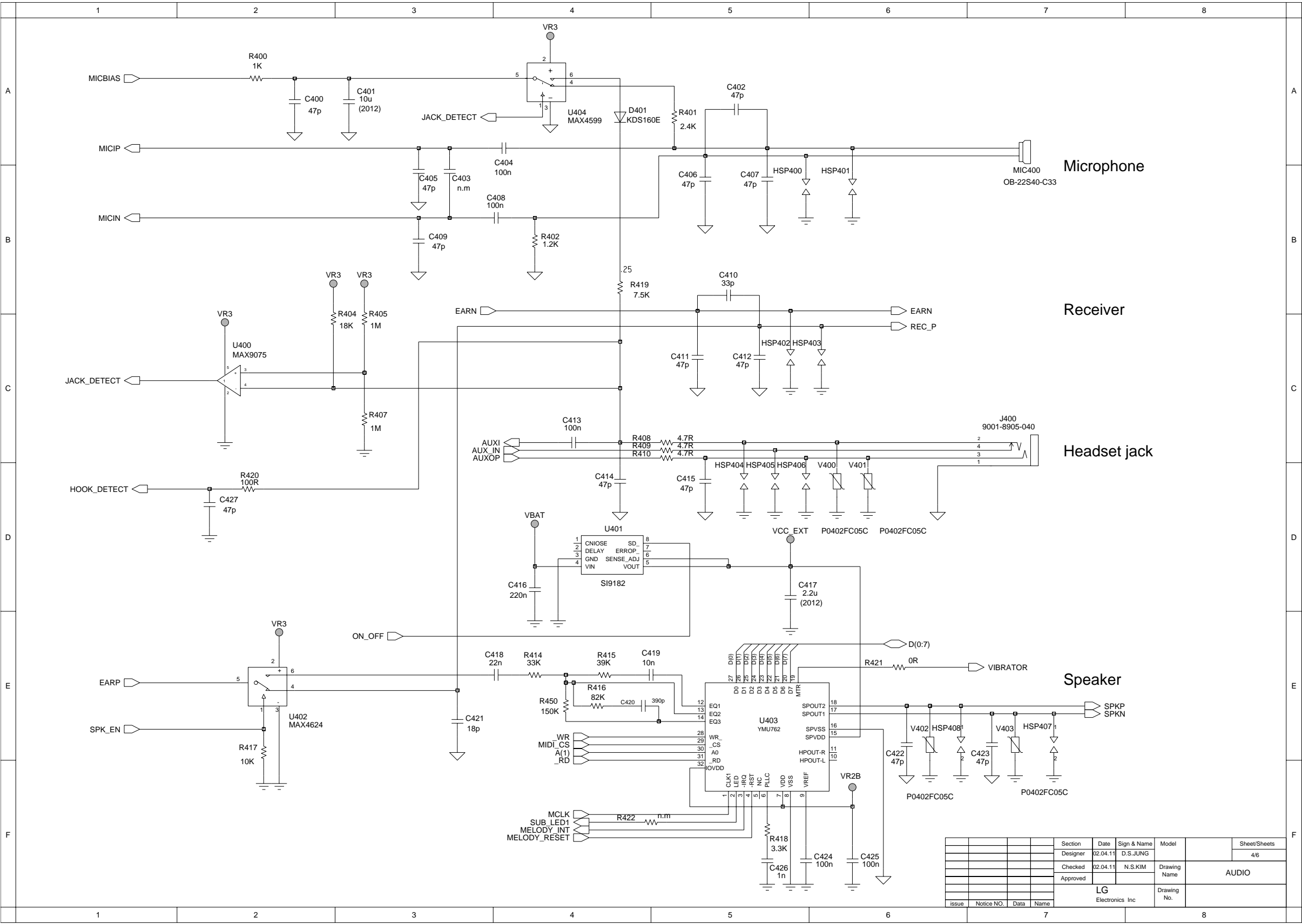
10. CIRCUIT DIAGRAM

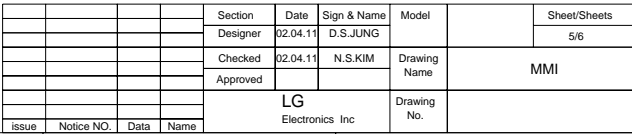




					Section	Date	Sign & Name	Model		Sheet/Sheets
					Designer	02.04.11	D.S.JUNG			3/6
					Checked	02.04.11	N.S.KIM	Drawing Name	MEMORY DEVICE	
					Approved			Drawing No.		
					LG					
					Electronics Inc.					
issue	Notice NO.	Data	Name							

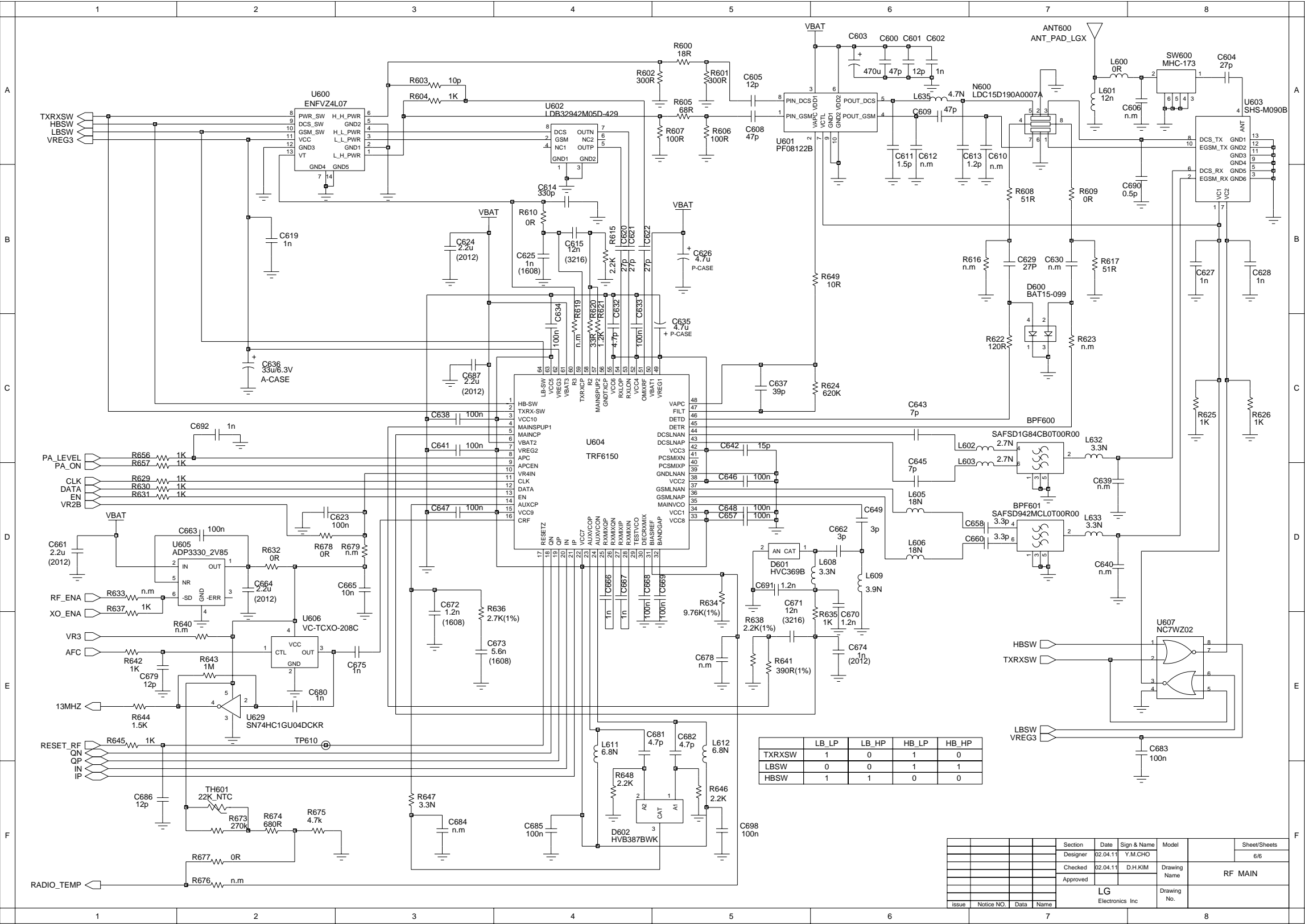
10. CIRCUIT DIAGRAM





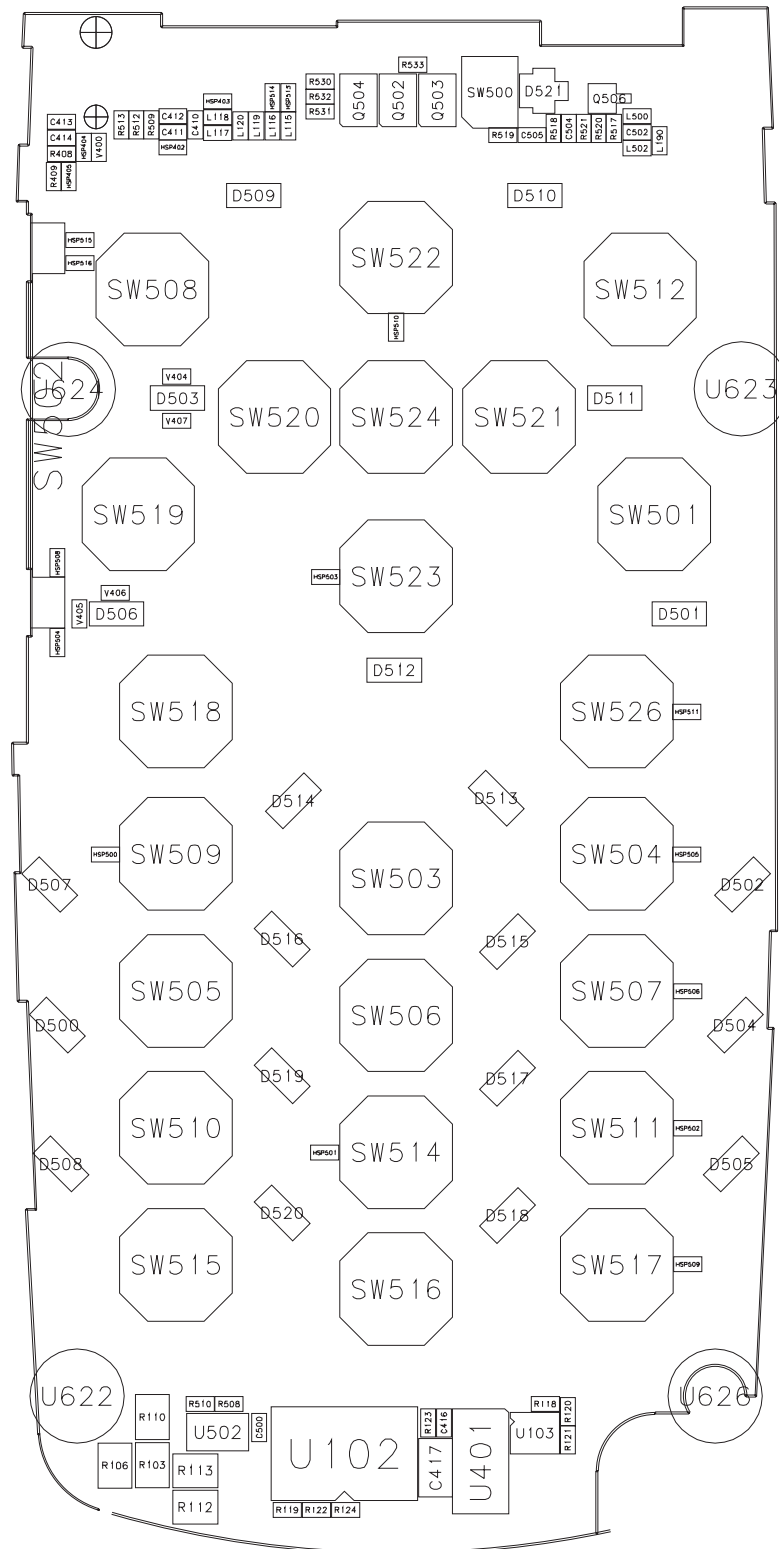
10. CIRCUIT DIAGRAM

10.2 RF Circuit



11. PCB LAYOUT

11.1 TOP



11. PCB LAYOUT

11.2 BOTTOM

